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(54) **METHOD AND APPARATUS FOR CONTROLLING SYNCHRONIZED PHASES IN SYSTEM FOR DRIVING PRINTING ROLLS FOR CORRUGATED BOARD PRINTING MACHINE.**

(57) The present invention relates to a technical field pertaining to control of synchronized phases between a plurality of printing rolls in a corrugated board printing machine. An object of the present invention is to realize the control of synchronized phases inexpensively and highly accurately by use of software. According to the present invention, speed commands inputted at regular intervals are converted into digital values and integrated, rotations of the rolls are respectively integrated, further, PI operations are conducted on the deviations of these integrated values, and corrections based on the results of the PI operations are respectively applied to rotational speed commands given to driving motors of the printing rolls. At this time, during the processes of integrations of the speed commands and the rotations of the respective printing rolls, when the upper limit values of the operations are exceeded, the values are to be returned to zero, respectively, and the operations are to be continued.

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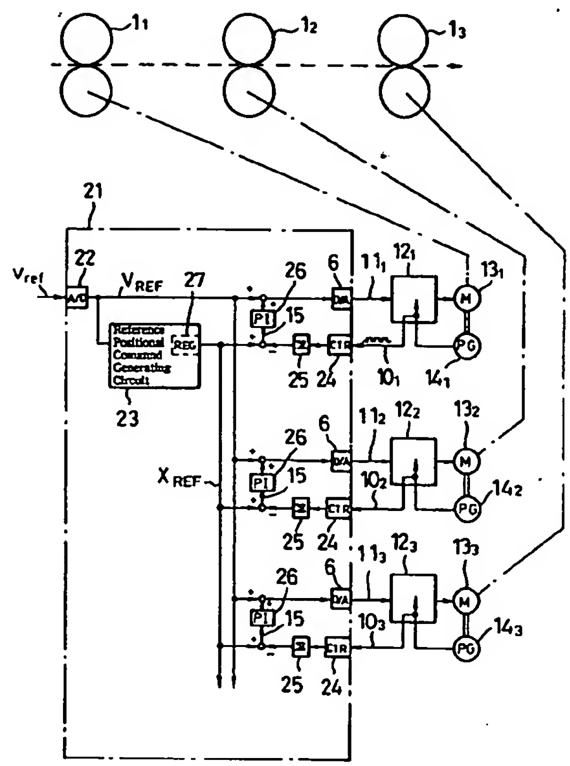

Reference Positional Command Generating Circuit
REG
PI
A/D
D/A
CTR
M
PG
Vref
VREF
XREF

Fig. 1

TECHNICAL FIELD

This invention relates to a method for controlling printing rolls in a corrugated board printing press and a system thereof, and specifically to a method of synchronously phase-controlling a printing roll driving system for a corrugated board printing press having a plurality of printing rolls in order to maintain a phase relationship between the printing rolls in a preset state, and a system thereof.

BACKGROUND ART

A corrugated board printing press is provided with a plurality of printing rolls to realize multi color printing. In order to avoid disadvantages such as misregistration, these printing rolls must be driven so as to synchronize their phases with each other. In order to maintain the phase relationship between these rolls unchanged, they have heretofore been coupled and interlocked with each other through a transmission such as belts and/or gears so as to be driven from a single motor having a variable speed and a large capacity. This arrangement however requires breaking the interlocking relation between the printing rolls when replacing plate cylinders installed on the printing rolls or maintaining the printing press and then recoupling them together into an operable state. This recoupling requires a great deal of work so that the gears are properly re-engaged with each other in order to keep the phase relationship between the printing rolls synchronous.

U.S. Patent No. 4,527,788 to Masuda, filed on August 1, 1984, discloses a printing press making use of a sectional servodrive method to overcome the above-described disadvantages. This apparatus comprises, on each printing roll, a DC drive motor having a variable speed, a zero point sensor for detecting a zero point marked on the roll to determine the revolution angle of the roll, a tachometer generator for detecting the speed of the DC drive motor and a pulse generator for generating pulses at a preset rate per predetermined revolution angle of the DC drive motor. First, the initial phase for each roll is determined by the zero point sensor to set it to a desired value. A speed command common to the individual DC drive motors is converted by a V/F converter to a reference pulse signal. This reference pulse signal is integrated and compared with an integrated pulse signal from the pulse generator, thereby determining a deviation. This deviation corresponds to the difference between the ideal and actual phases of the printing roll. Using an analog computer, the F/V-converted reference pulse signal is compared with the revolution speed of the DC drive motor to determine a servo-controlling value. Further, the level of servo-controlling is adjusted according to the degree of the phase deviation, whereby the DC drive motor is servo-controlled.

In FIG. 4 is shown an illustrative system obtained by further improving on the system disclosed in U.S. Patent No. 4,527,788. FIG. 4 is a block diagram illustrating the construction of a synchronous phase-control system for printing rolls in a corrugated board printing press having, for example, 3 printing rolls.

Three printing rolls 1_1, 1_2, and 1_3 are driven by servomotors 13_1, 13_2 and 13_2, respectively. Pulse encoders 14_1, 14_2 and 14_3 respectively connected to servomotors 13_1, 13_2 and 13_3 output positional feedback pulse signals 10_1, 10_2 and 10_3, respectively, according to the revolution of their corresponding servomotors 13_1, 13_2 and 13_3. Positional feedback pulse signals 10_1, 10_2 and 10_3 are inputted as feedback N_{FB} in respective servodrivers 12_1, 12_2 and 12_3 through their corresponding F/V converters 8 and at the same time, also in their corresponding deviation counters 5. The term "synchronous phase-control" as used herein means that in this apparatus of the sectional servodrive system, the phase relationship between the rotors of the individual servomotors at the beginning of operation is kept unchanged during operation.

For this purpose, reference positional command pulse signal 9 is generated by pulse generator 3 according to speed command v_{ref} inputted from speed setter 2. Any deviation between this signal and positional feed back pulse signal 10_1 is detected by deviation counter 5 and outputted as positional deviation signal 15. Deviation counter 5 comprises phase pulse counter 5a, pulse computing circuit 5b and reference pulse counter 5c and is conventionally known. After positional deviation signal 15 is D/A-converted by D/A converter 6, the gain of the analog signal thus converted is adjusted by analog regulator 7. The analog signal thus adjusted is added to an analog speed command converted from reference positional command pulse signal 9 through F/V converter 4. The sum is given as revolution speed command 11 for servomotor 13_1 to servodriver 12_1, whereby servodriver 12_1 serves to drive servomotor 13_1. To other servomotors 13_2 and 13_3, respective revolution speed commands $11_2$and 11_3 are also given by control units similar to that described above, so that each of servomotors 13_1 to 13_3 is synchronously phase-controlled in such a manner that the deviation of the actual revolution from the positional command generated by common speed command v_{ref} becomes zero.

DISCLOSURE OF INVENTION

The above-described conventional synchronous phase control system according to the sectional servodrive method in the printing press for corrugated boards requires hardware such as a pulse train generator, an F/V converter and an operational amplifier in order to form a reference positional command. With recent development in microprocessors, attempts to realize various kinds of control equipment by software, which were heretofore actualized by hardware, have been made in various places. However, it has been unfeasible to date to make up the synchronous phase control systems described above of software for the following reasons. Pulses which represent the movement of the printing rolls must be integrated continuously because their movement is rotary, and it is hence impossible to avoid problems of overflow of the numeric value, and of numerical expression (for example, according to the numerical expression in a controller conventionally used in the system of this kind, the negative maximum value appears next to the positive maximum value), among others. Moreover, controllers having a CPU, which permits high-speed computing processes, are rarely available.

In view of the foregoing circumstances, the present invention has been made and is intended to realize the synchronous phase control of printing rolls in a corrugated board printing press by making use of software in place of hardware. In other words, an object of this invention is to provide a method of synchronously phase-controlling a printing roll drive system for a corrugated board printing press by making use of software, said method being high in precision and permitting an increase in the number of rolls without a substantial increase in cost, and a system suitable for use in performing this method.

According to the present invention, there is thus provided a method of synchronously phase-controlling a printing roll drive system for a corrugated board printing press, which comprises converting a common speed command inputted in each of the printing rolls to its corresponding internal speed command, forming a positional feedback pulse signal by a pulse encoder connected to its corresponding printing roll, detecting and integrating the internal speed command at regular intervals to multiply a predetermined coefficient by the integrated value each time, thereby forming a reference positional command, said reference positional command returning to 0 after it comes to the highest value of the operation to continue the integration, integrating the positional feedback pulse signal to form a feedback position signal, said feedback position signal returning to 0 after it comes to the highest value of the operation to continue the integration, and then subjecting any deviation between the reference positional command and the feedback position signal to PI operation to add its result to the speed command, thereby regarding the sum as a revolution speed command to the corresponding printing roll to drive the corresponding printing roll according to the revolution speed command.

According to this invention, there is also provided a system for synchronously phase-controlling a printing roll drive system for a corrugated board printing press, which comprises a controller having a reference positional command generating circuit for converting a common speed command inputted in each of the printing rolls to its corresponding internal speed command, detecting and integrating the internal speed command at regular intervals to multiply a predetermined coefficient by the integrated value each time, thereby forming a reference positional command, said reference positional command returning to 0 after it comes to the highest value of the operation to continue the integration, feedback position signal forming means for separately counting and integrating positional feedback pulse signals of the printing rolls to form their corresponding feedback position signals, each of said feedback position signals returning to 0 after it comes to the highest value of the operation to continue the integration, and PI-operating means for separately subjecting deviations between the reference positional commands and the feedback position signals in the printing rolls to PI operation to add their results to their corresponding internal speed commands and outputting the sums as a revolution speed command in their corresponding servo-controllers.

In other words in this invention, a speed command inputted is detected and integrated at regular intervals to multiply a predetermined coefficient by the integrated value each time, thereby forming a reference positional command moment by moment. This reference positional command returns to 0 after it comes to the highest value of the operation to continue the operation. These operation processes can be executed by means of a controller realized by a CPU. How the above-mentioned reference positional command is formed will hereinafter be described with reference to FIGS. 2(a) and 2(b).

Supposing that an internal speed command corresponding to inputted speed command v_{ref}, a reference positional command and a predetermined coefficient are V_{REF}, X_{REF} and A, respectively, the operation process by the CPU is expressed by:

of internal speed command V_{REF}. Area R indicated by cross oblique lines corresponds to feedback pulses for 1 T_S (600 pulses in the case of the above-described calculation). In addition, FIG. 2(b) illustrates the condition in which the increment in reference positional command X_{REF} operated at every interval T_S is integrated serially.

Similarly, positional feedback pulses fed back from a pulse encoder connected to each printing roll are integrated at the regular intervals described above to form a feedback position signal moment by moment. This feedback position signal returns to 0 after it comes to the highest value of the operation to continue the operation. As described above, numerical continuity upon integration is given to the controller, whereby in the operation as to any deviation between the reference positional command and the feedback position signal, continuity of operation can be achieved even in the vicinity of the upper limit or 0 of the register used. Therefore, this deviation is subjected to PI operation and then added to the speed command to control the revolution speed of a drive motor through a servodriver in such a manner that the deviation becomes 0. At this time, it is convenient to carry out not only P (proportional) control proportional to the deviation but also I (integral) control, because an operational output corresponding to torque required upon acceleration or deceleration of the speed can be obtained. The above-described control making use of the common speed command to all the drive motors permits the realization of synchronous phase control. In addition, the execution of the whole operation by the CPU permits the provision of a system which is both reliable in operation and cheap.

BRIEF DESCRIPTION OF THE DRAWING

FIG. 1 is a block diagram illustrating the constitution of a synchronous phase control system of a servo system according to an embodiment of this invention;

FIG. 2(a) is a characteristic diagram illustrating changes in internal speed command V_{REF}, and FIG. 2(b) is a characteristic diagram illustrating formation of reference positional command X_{REF};

FIG. 3 is a characteristic diagram illustrating how to integrate both reference positional command X_{REF} and a feedback position signal; and

FIG. 4 is a block diagram illustrating the constitution of a conventional synchronous phase control system for printing rolls in a corrugated board printing press having, for example, 3 printing rolls.

BEST MODE OF CARRYING OUT THE INVENTION

Embodiments according to the present invention will hereinafter be described with reference to the drawings.

FIG. 1 is a block diagram illustrating the constitution of a synchronous phase control system making use of a method for synchronously phase-controlling a printing roll driving system for a corrugated board printing press according to an embodiment of this invention.

In this embodiment, a series of hardware ranging from pulse train generator 3 to adding operational amplifiers 8, which have been used in the prior art described above with reference to FIG. 4, are replaced by controller 21 composed of a CPU. In FIG. 1, controller 21 is represented by a circuit diagram as a matter of convenience for the purpose of explaining the contents of operation executed by controller 21.

Three printing rolls 1_1, 1_2 and 1_3 are connected to driving servomotors 13_1, 13_2 and 13_3, respectively. Servomotors 13_1, 13_2 and 13_3 are respectively driven through servodrivers 12_1, 12_2 and 12_3 and directly connected to pulse encoders 14_1, 14_2 and 14_3. These pulse encoders 14_1, 14_2 and 14_3 are adapted to generate respective positional feedback pulses 10_1, 10_2 and 10_3 whenever servomotors 13_1, 13_2 and 13_3 rotate by a predetermined angle, i.e., whenever printing rolls 1_1, 1_2 and 1_3 rotate by the predetermined angle.

Controller 21 comprises A/D converter 22 and reference positional command generating circuit 23, which are commonly provided for printing rolls 1_1, 1_2 and 1_3, and D/A converters 6, counters 24, positional feedback pulse generating circuits 25 and PI computing circuits 26, which are separately provided on each of printing rolls 1_1, 1_2 and 1_3. A/D converter 22 is adapted to convert speed command v_{ref}, which is an analog signal fed from the outside for indicating the revolution speed of each of printing rolls 1_1, 1_2 and 1_3, to internal speed command V_{REF}, which is a digital signal used in controller 21. Internal speed command V_{REF} is inputted in reference position command generating circuit 23, and then for each of printing rolls 1_1, 1_2 and 1_3, added to an output from its corresponding PI computing circuit 26, which will be described subsequently, to be inputted in its corresponding D/A converter 6. Each of D/A converters 6 D/A-converts the input signal to a revolution speed command 11_1, 11_2 or 11_3 inputted in its corresponding servodriver 12_1, 12_2 or 12_3.

Reference positional command generating circuit 23 contains register 27 having a predetermined bit length therein, and is adapted to detect and integrate internal speed command V_{REF} at regular intervals, store a product obtained by multiplying above-described coefficient A by this integrated value in register 27 and output the data stored in register 27 as reference positional command X_{REF}. In this case, after reference positional command X_{REF} comes to the highest value depending upon the bit length of register 27, the value of (the highest value + 1) is regarded as 0 to continue the integration. In other words, integration in this register 27 is executed without consideration for the so-called sign bit and in disregard of overflow.

On the other hand, counters 24 in which their corresponding positional feedback pulses 10_1, 10_2 and 10_3 are inputted through respective servodrivers 12_1, 12_2 and 12_3 are adapted to count positional feedback pulses 10_1, 10_2 and 10_3 at the same intervals as the integration in reference positional command generating circuit 23 and to send the counts to their corresponding positional feedback pulse integrating circuits 25. Positional feedback pulse integrating circuits 25 each have the same bit length as that of register 27 and are adapted to integrate their corresponding counts of positional feedback pulses 10_1, 10_2 and 10_3 at the same intervals as the integration in reference positional command generating circuit 23. In this integration as well, after the integrated value comes to the highest value depending upon the bit length of feedback pulse integrating circuit 25, the value of (the highest value + 1) is regarded as 0 to continue the integration, as in the case of reference positional command X_{REF}. Regarding this integrated value, its deviation 15 from reference positional command X_{REF} is found. Deviation 15 is inputted in its corresponding PI computing circuit 26. As described below, the calculation for finding deviation 15 is executed with consideration for the so-called sign bit so as to be able to process positive and negative numbers. PI computing circuits 26 are those well known in the art for performing PI (proportional plus integral) control.

The computing operation of this system will hereinafter be described.

A speed command V_{ref} inputted for indicating the revolution speed of printing rolls 1_1, 1_2 and 1_3 is converted to corresponding internal speed command V_{REF} by A/D converter 22 and inputted in reference positional command generating circuit 23. This internal speed command V_{REF} is used as a reference of the speed upon driving servomotors 13_1, 13_2 and 13_3. Reference positional command generating circuit 23 serves to detect internal speed command V_{REF} inputted at regular intervals, integrate it serially to multiply above-described coefficient A by this integrated value, and then store the product each time in register 27 to output it as reference positional command X_{REF}. In this case, as described above, reference positional command X_{REF} returns to 0 after it comes to the highest value to continue the integration. Therefore, reference positional command X_{REF} always represents a fraction where an integrated revolution angle determined for each of printing rolls 1_1, 1_2 and 1_3 is divided by a fixed number. This fixed number is a value corresponding to the bit length of register 27.

On the other hand, positional feedback pulses 10_1, 10_2 or 10_3 from pulse encoders 14_1, 14_2 or 14_3 are counted by its corresponding counter 24. This count is integrated in positional feedback pulse integrating circuit 25 at the same intervals as to the case of the detection of internal speed command V_{REF} described above. This integrated value is a feedback position signal, which represents a fraction where the actual integrated revolution angle of each of printing rolls 1_1, 1_2 and 1_3 is divided by a fixed number. This fixed number is the same as that in the case of reference positional command X_{REF} described above. Any deviation between the feedback position signal and reference positional command X_{REF} represents the difference between the actual revolution angle of its corresponding printing roll 1_1, 1_2 or 1_3 and the revolution angle based on the speed command at that point of time. Therefore, the speed to be commanded to its corresponding servomotor 13_1, 13_2 or 13_3 will be accelerated or decelerated by a degree corresponding to the deviation of the revolution angle by executing PI operation in PI computing circuit 26 on the basis of this deviation and adding the result to internal speed command V_{REF}.

Internal speed command V_{REF} added to the result of the PI operation is D/A-converted by D/A converter 6 to revolution speed command 11_1, 11_2 or 11_3, which is to be outputted to its corresponding servodriver 12_1, 12_2 or 12_3. Servodriver 12_1, 12_2 or 12_3 serves to drive its corresponding servomotor 13_1, 13_2 or 13_3 according to revolution speed command 11_1, 11_2 or 11_3. Servomotor 13_1, 13_2 or 13_3 is driven according to a command obtained by using common internal speed command V_{REF} as a reference and correcting common internal speed command V_{REF} on the basis of the deviation between its corresponding feedback position signal and reference positional command X_{REF}. Therefore, servomotor 13_1, 13_2 and 13_3 are driven so that their phases will synchronize with one another according to speed command v_{ref}.

Here, how to find the deviation between the feedback position signal and reference positional command X_{REF} will be described. As described above, the feedback position signal and reference positional command X_{REF} have the same bit length and are integrated by regarding values, (the highest value + 1), of respective positional feedback pulse generating circuit 25 and register 27 as 0 upon their integration. The description will hereinafter be given about reference positional command X_{REF} by considering the bit length to be 16

bits. It goes without saying that this applies exactly to the case of the feedback position signal. This integration does not take the sign bits into consideration as described above, and is hence processed as so-called unsigned integer operation. The highest value expressed as a number is 65535 (= 2^{16} - 1). Here, if hexadecimal notation is expressed in $_{(H)}$, the highest value, 65535, is $FFFF_{(H)}$. In addition, the internal expression in both positional feedback pulse generating circuit 25 and register 27 is $FFFF_{(H)}$. For example, $4_{(H)}$ as a new value to be integrated, ΔX_{REF}, is integrated to this number, resulting in $FFFF_{(H)} + 4_{(H)} = 10003_{(H)}$ as illustrated in FIG. 3. Since (the highest value + 1) is regarded as 0, however, it makes $3_{(H)}$.

In the deviation on the other hand, the computation is made by so-called signed integer operation in which positive and negative numbers are distinguished from each other as described above. The signed integer operation is an operation wherein if the most significant bit is 0, the value represents a positive number, while if the most significant bit is 1, the value represents a negative number. The reason why this operation is used is that neither the feedback position signal nor reference positional command X_{REF} becomes negative, whereas the deviation can have either positive and negative values. The deviation is obtained by subtracting the feedback position signal from reference positional command X_{REF}. For example, when reference positional command X_{REF} and the feedback position signal are $FFFF_{(H)}$ and $3_{(H)}$, respectively, the deviation is to be $-4_{(H)}$ from the above description. Subtracting $3_{(H)}$ from $FFFF_{(H)}$ leaves $FFFC_{(H)}$ in terms of the internal expression. This means 65532 in the unsigned operation and $-4 (= -4_{(H)})$ in the signed operation. It is to be understood that a positive or negative deviation can be found exactly in a range of numbers (if 16 bits, from -32768 to +32767), which are represented by the bit length where the most significant bit is made a sign bit, by executing the signed operation.

The bit lengths of the feedback position signal and reference positional command X_{REF} will hereinafter be described. Controller 21 shown in this embodiment usually consists of a CPU. CPUs are usually constructed so as to permit the operation of 8, 16 or 32 bits, or even longer bits. If 16 bits are made single-length data and 32 bits double-length data, it is possible to process numbers in a range of from -32768 to +32767 for the single-length data and from -2147483648 to +2147483647 for the double-length data. If the number of positional feedback pulses per revolution of each of printing rolls 1_1, 1_2 and 1_3 is made greater in order to enhance the resolution of the system, the value of the deviation in the single-length data may momentarily depart from the above range when a great load change occurs. For example, in U.S. Patent No. 4,527,788 cited above, there is a description to the effect that 15,000 pulses per revolution of the drive motor are generated. Such a departure of the deviation from the range may cause an operation error, so that the synchronous phase relationship between printing rolls 1_1, 1_2 and 1_3 may be discontinued, resulting in unavoidable stop of the printing press. Further, since the data in the course of operation must fall within the range of the data length, it is desirable that the bit lengths of the feedback position signal and reference position signal X_{REF}, i.e., the bit lengths of positional feedback pulse generating circuit 25 and register 27, are made 32 bits (double-length data) or more.

In the above description, the progress of the operation has been explained as represented by the circuit diagram illustrated in FIG. 1 as a matter of convenience. In reality, the whole operation is however executed according to a program stored in controller 21 (CPU).

INDUSTRIAL APPLICABILITY

As described above, the present invention brings about the following effects. Since the whole operation is executed by means of a CPU which is a controller capable of carrying out the processes to maintain numerical continuity upon integration, the mere application of a speed command to the controller from the outside permits synchronous phase control, and no hardware incident to the outside is required. In addition, since all the processes are carried out by digital software, it is also possible to use double-length data. It is hence possible to synchronously phase-control the printing roll drive systems for corrugated board printing presses with high precision and without a substantial increase in cost even when the number of drive shafts to be controlled is increased.

Claims

1. A method of synchronously phase-controlling a printing roll drive system for a corrugated board printing press having a plurality of printing rolls in order to maintain a phase relationship between the printing rolls in a preset state, which comprises:

   converting a common speed command inputted in each of the printing rolls to its corresponding internal speed command,

   forming a positional feedback pulse signal by a pulse encoder connected to its corresponding

printing roll,

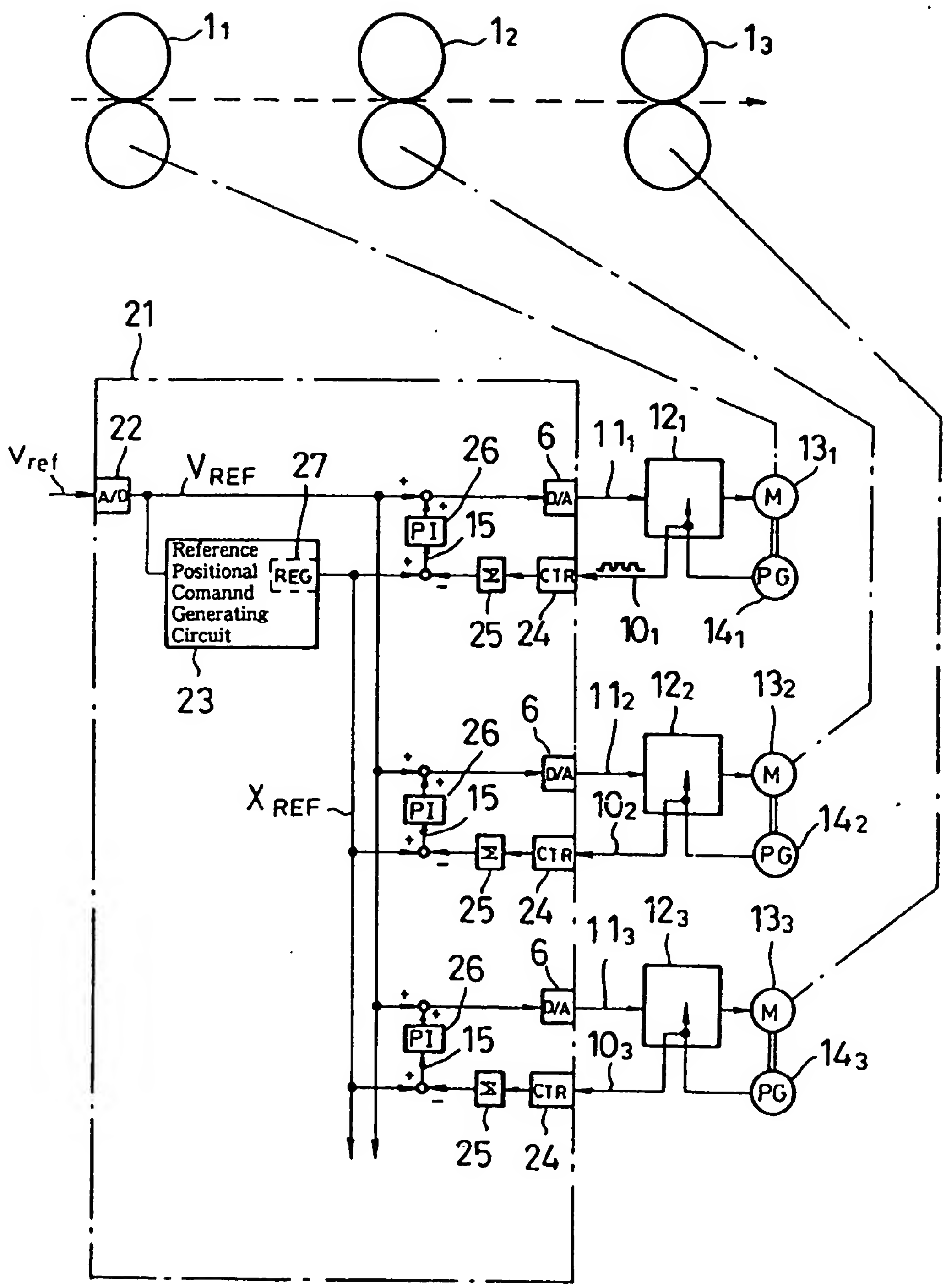
detecting and integrating the internal speed command at regular intervals to multiply a predetermined coefficient by the integrated value each time, thereby forming a reference positional command, said reference positional command returning to 0 after it comes to the highest value of the operation to continue the integration,

integrating the positional feedback pulse signal to form a feedback position signal, said feedback position signal returning to 0 after it comes to the highest value of the operation to continue the integration, and then

subjecting any deviation between the reference positional command and the feedback position signal to PI operation to add its result to the speed command, thereby regarding the sum as a revolution speed command to the corresponding printing roll to drive the corresponding printing roll according to the revolution speed command.

2. A system for synchronously phase-controlling a printing roll drive system for a corrugated board printing press having a plurality of printing rolls in order to maintain a phase relationship between the printing rolls in a preset state, said printing roll drive system comprising drive motors which are separately connected to the printing rolls, pulse encoders which are separately connected to the printing rolls and which generate a positional feedback pulse signal, and servodrivers which are separately provided on the drive motors and which serve to drive their corresponding drive motors, which comprises:

a controller having a reference positional command generating circuit for converting a common speed command inputted in each of the printing rolls to its corresponding internal speed command, detecting and integrating the internal speed command at regular intervals to multiply a predetermined coefficient by the integrated value each time, thereby forming a reference positional command, said reference positional command returning to 0 after it comes to the highest value of the operation to continue the integration; feedback position signal forming means for separately counting and integrating positional feedback pulses of the printing rolls to form their corresponding feedback position signals, each of said feedback position signals returning to 0 after it comes to the highest value of the operation to continue the integration; and PI-operating means for separately subjecting deviations between the reference positional commands and the feedback position signals in the printing rolls to PI operation to add their results to their corresponding internal speed commands and outputting the sums as a revolution speed command in their corresponding servocontrollers.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Positional
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Generating
Circuit
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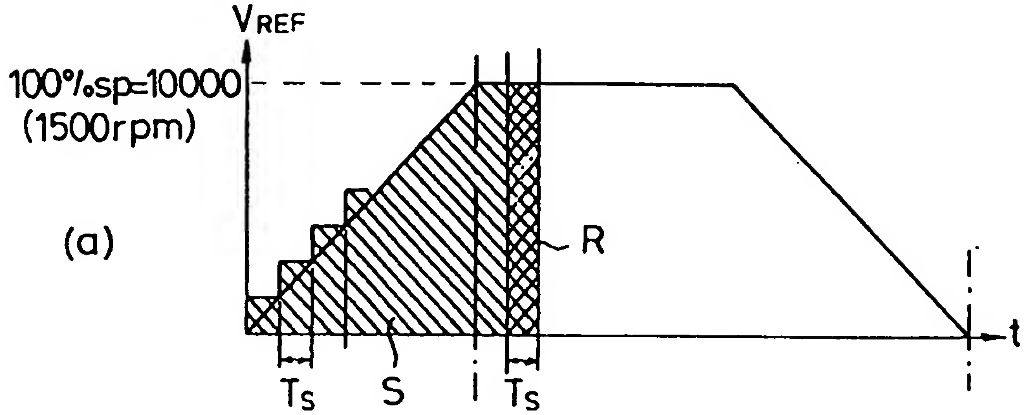
Fig. 1

(a)



(b)

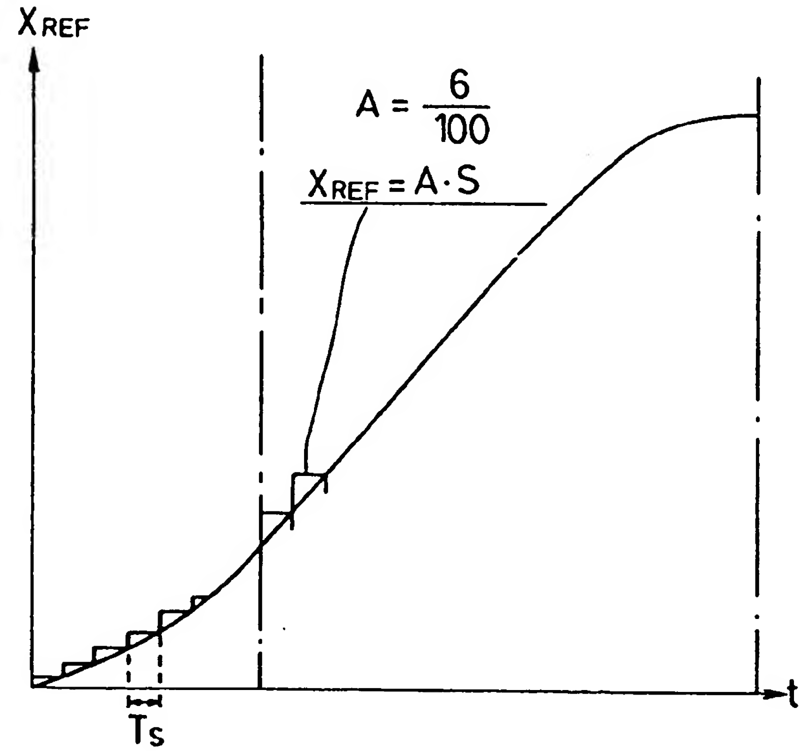


Fig. 2

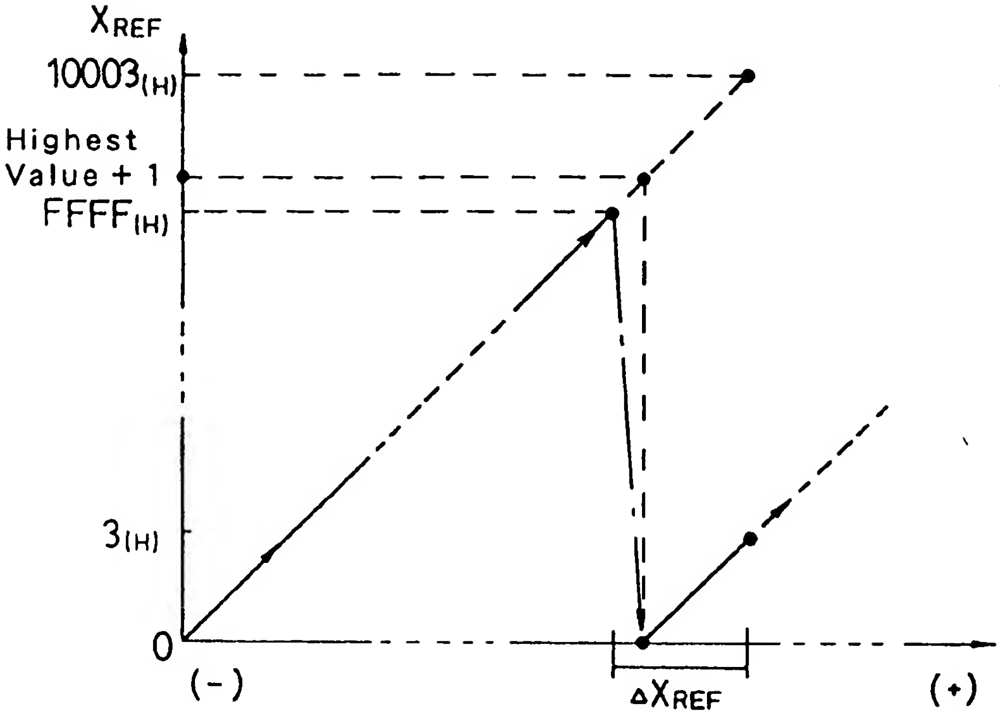


Fig. 3

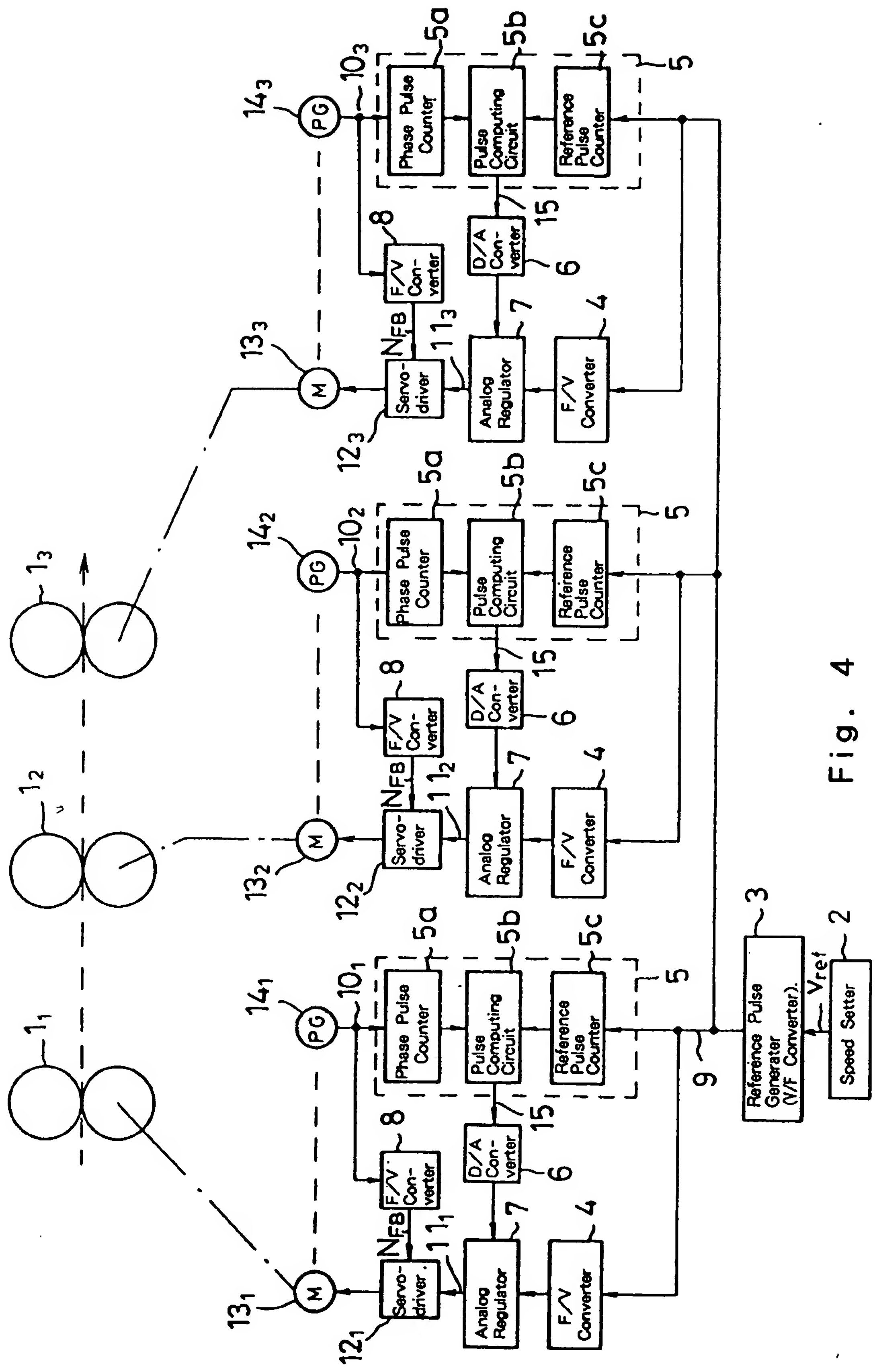

Speed Setter
Reference Pulse Generater (V/F Converter)
Vref
Phase Pulse Counter
Pulse Computing Circuit
Reference Pulse Counter
D/A Con-verter
F/V Con-verter
Servo-driver
Analog Regulator
F/V Converter
PG
M
NFB

Fig. 4

INTERNATIONAL SEARCH REPORT

International Application No PCT/JP91/00963

I. CLASSIFICATION OF SUBJECT MATTER (if several classification symbols apply, indicate all) [6]

According to International Patent Classification (IPC) or to both National Classification and IPC

Int. Cl^5 B41F33/08, H02P5/00, G05D13/62

II. FIELDS SEARCHED

Minimum Documentation Searched [7]

Classification System	Classification Symbols
IPC	B41F33/00, H02F5/00, G05D13/62

Documentation Searched other than Minimum Documentation to the Extent that such Documents are Included in the Fields Searched [8]

Jitsuyo Shinan Koho	1926 - 1991
Kokai Jitsuyo Shinan Koho	1971 - 1991

III. DOCUMENTS CONSIDERED TO BE RELEVANT [9]

Category [*]	Citation of Document, [11] with indication, where appropriate, of the relevant passages [12]	Relevant to Claim No. [13]
A	JP, A, 60-250955 (Hamada Insatsuki Sezosho K.K.), December 11, 1985 (11. 12. 85)	1, 2
A	JP, A, 55-71188 (Toshiba Corp.), May 29, 1980 (29. 05. 80)	1, 2

* Special categories of cited documents: [10]

"A" document defining the general state of the art which is not considered to be of particular relevance

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"O" document referring to an oral disclosure, use, exhibition or other means

"P" document published prior to the international filing date but later than the priority date claimed

"T" later document published after the international filing date or priority date and not in conflict with the application but cited to understand the principle or theory underlying the invention

"X" document of particular relevance; the claimed invention cannot be considered novel or cannot be considered to involve an inventive step

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IV. CERTIFICATION

Date of the Actual Completion of the International Search	Date of Mailing of this International Search Report
October 7, 1991 (07. 10. 91)	October 28, 1991 (28. 10. 91)
International Searching Authority	**Signature of Authorized Officer**
Japanese Patent Office	